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Date: September 12, 2002

S. McVean  
Sonia V. McVean

**PATENT**  
36856.636

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

In re application of:  Yoshiyuki TONAMI et al.  Serial No.: 10/087,915  Filing Date: March 5, 2002  For: HIGH FREQUENCY CIRCUIT CHIP AND METHOD OF PRODUCING THE SAME	Art Unit: 2811  Examiner: O. Nadav
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**RESPONSE TO RESTRICTION REQUIREMENT**

Assistant Commissioner for Patents  
Washington, D.C. 20231

Dear Sir:

In response to the Restriction Requirement issued on September 5, 2002, Applicants elect Group II, including Claims 1-8, 10 and 12 for prosecution in the present application. This election is made without traverse.

Applicants hereby reserve the right to file a Divisional Application to pursue prosecution of the non-elected claims 9, 11 and 13-17.

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Serial No. 10/087,915

Art Unit 2811

September 12, 2002

Applicants respectfully submit that this application is in condition for allowance. Favorable consideration and prompt allowance of the present application are respectfully requested.

Respectfully submitted,

Date: September 12, 2002

  
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